

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|--|-------------------|------------------|---------|------------------|
| S1 | 25 | logic adj circuit with first adj reset with second adj reset | USPAT | OR | OFF | 2009/04/03 14:08 |
| S2 | 0 | logic adj circuit with first adj reset with second adj reset with (charg\$3 discharg\$3) | USPAT | OR | OFF | 2009/04/03 14:09 |
| S3 | 0 | logic adj circuit with first adj reset with second adj reset same (charg\$3 discharg\$3) | USPAT | OR | OFF | 2009/04/03 14:33 |
| S4 | 14 | logic adj circuit with first adj reset with second adj reset and (charg\$3 discharg\$3) | USPAT | OR | OFF | 2009/04/03 14:33 |
| S5 | 9 | logic adj circuit with first adj reset with second adj reset and (charg\$3 discharg\$3) | US-PGPUB | OR | OFF | 2009/04/03 14:48 |
| S6 | 0 | logic adj circuit with first adj reset with second adj reset and (charg\$3 discharg\$3) | EPO; JPO; IBM_TDB | OR | OFF | 2009/04/03 14:50 |

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|-----|----|--|-------------------|----|-----|------------------|
| S7 | 23 | monitoring adj circuit and logic adj circuit with reset adj signal | USPAT | OR | OFF | 2009/04/03 15:09 |
| S8 | 0 | monitoring adj circuit and logic adj circuit with first adj reset adj signal | USPAT | OR | OFF | 2009/04/03 15:09 |
| S9 | 12 | monitoring adj circuit and logic adj circuit with reset adj signal | US-PGPUB | OR | OFF | 2009/04/03 15:34 |
| S10 | 2 | monitoring adj circuit and logic adj circuit with reset adj signal | EPO; JPO; IBM_TDB | OR | OFF | 2009/04/03 15:37 |
| S11 | 16 | logic adj circuit with reset adj signal with inputted | USPAT | OR | OFF | 2009/04/03 15:42 |
| S12 | 12 | logic adj circuit with reset adj signal with inputted | US-PGPUB | OR | OFF | 2009/04/03 15:51 |
| S13 | 11 | logic adj circuit with reset adj signal with inputted | EPO; JPO; IBM_TDB | OR | OFF | 2009/04/03 15:55 |

4/3/2009 5:31:25 PM

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